

U.S. Department of Commerce, Patent and Trademark Office					Attorney Docket No.	Serial No.		
					SUN040292	10/790,650		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicant(s)			
(Use several sheets if necessary)					Harinath B. Kamepalli et al.			
					Filing Date	Group 2825		
					March 1, 2004	Unknown		
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
PD	AA	6,634,012	10/14/2003	Zhong et al.	716	5		
	AB	6,631,470	10/07/2003	Chang et al.	713	3		
	AC	6,574,762	06/03/2003	Karimi et al.	714	727		
	AD	6,327,687	12/04/2001	Rajski et al.	714	738		
	AE	6,134,689	10/17/2000	Mateja et al.	714	736		
	AF	2003/0204802A1	10/30/2003	Sim	714	726		
	AG	2003/0131298A1	07/10/2003	Rajski et al.	714	738		
	AH	2003/0065996A1	04/03/2003	Shimada et al.	714	718		
PD	AI	2002/0053057A1	05/02/2002	Rajski et al.	714	738		
	AJ							
	AK							
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL						<input type="checkbox"/>	<input type="checkbox"/>
	AM						<input type="checkbox"/>	<input type="checkbox"/>
	AN						<input type="checkbox"/>	<input type="checkbox"/>
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
PD	AR	Takahashi, Tsuyoshi, et al., "Design Flow and Design Environment for System LSIs," Hitachi Review Vol. 48 (1999) No. 2, pp. 87-93						
PD	AS	Kurup, Pran, et al., "Mastering HDL for formal verification," Electronics Engineer, May 1999						
PD	AT	http://www.stridge.com/Stridge_articles/atpg_model_oview.html , "Memory ATPG Modeling and Validation Process"						
Examiner		PAUL DINH		Date Considered		7/13/05		
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>								

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	AN						<input type="checkbox"/>	<input type="checkbox"/>
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
PD	AR	http://www.synopsys.com/products/acmgr/innlo/ , "Former InnoLogic Systems Products"						
PD	AS	http://www.synopsys.com/products/acmgr/innlo/espcv.html , "ESP-CV"						
PD	AT	http://www.synopsys.com/products/acmgr/innlo/esplv.html , "ESP-LV"						
Examiner		PAUL VINH		Date Considered		7/13/05		
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		Document	Date	Country	Class	Subclass	Yes	No
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	AN						<input type="checkbox"/>	<input type="checkbox"/>
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
PD	AR	http://www.synopsys.com/products/acmgr/innlo/espbv.html , "ESP-BV"						
PD	AS	http://www.synopsys.com/products/acmgr/innlo/ckt.html , "CKT"						
PD	AT	http://www.synopsys.com/products/acmgr/innlo/casestudy/suncase.html , "Case Study"						
Examiner		PAUL DINH		Date Considered		7/13/05		
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